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## \*BIBDATASHEET\*

Bib Data Sheet

CONFIRMATION NO. 7542

SERIAL NUMBER 10/726,492	FILING DATE 12/04/2003 RULE	CLASS 711	GROUP ART UNIT 2188	ATTORNEY DOCKET NO. 02207/868603
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## APPLICANTS

Nhon T. Quach, San Jose, CA;

John H. Crawford, Los Gatos, CA;

Greg S. Mathews, Santa Clara, CA; Edward Grochowski, San Jose, CA;  
Chakravarthy Kosaraju, Sunnyvale, CA;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/750,094 12/29/2000 PAT 6,675,266  
 and is a DIV of 10/435,386 05/12/2003 PAT 6,775,746  
 which is a DIV of 09/750,094 12/29/2000 PAT 6,675,266

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/15/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY CA	SHEETS DRAWING 7	TOTAL CLAIMS 36	INDEPENDENT CLAIMS 4
Verified and Acknowledged Examiner's Signature	Initials				

## ADDRESS

23838  
 KENYON & KENYON  
 1500 K STREET, N.W., SUITE 700  
 WASHINGTON , DC  
 20005

## TITLE

Circuit and method for protecting 1-hot and 2-hot vector tags in high performance microprocessors

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